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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO	
10/601,959	06/23/2003	Chang-Hyeon Lee	050324-1321 8906		
24504	7590 02/28/2005		EXAMINER		
THOMAS, KAYDEN, HORSTEMEYER & RISLEY, LLP 100 GALLERIA PARKWAY, NW			CUNNINGHAM, TERRY D		
STE 1750	MATAKWAT, NW	ART UNIT	PAPER NUMBER		
ATLANTA,	GA 30339-5948		2816		
			DATE MAILED: 02/28/2005	5	

Please find below and/or attached an Office communication concerning this application or proceeding.

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		Application I	NO.	Applicant(s)				
Office Action Summary		10/601,959		LEE ET AL.				
		Examiner		Art Unit				
		Terry D. Cunr	-	2816				
Period fo	The MAILING DATE of this communication app or Reply	pears on the co	ver sheet with the c	orrespondence ad	idress			
THE - Exter after - If the - If NO - Failu Any r	ORTENED STATUTORY PERIOD FOR REPL' MAILING DATE OF THIS COMMUNICATION. sions of time may be available under the provisions of 37 CFR 1.1: SIX (6) MONTHS from the mailing date of this communication. period for reply specified above is less than thirty (30) days, a reply period for reply is specified above, the maximum statutory period v re to reply within the set or extended period for reply will, by statute eply received by the Office later than three months after the mailing and patent term adjustment. See 37 CFR 1.704(b).	36(a). In no event, h y within the statutory vill apply and will ex , cause the applicati	nowever, may a reply be tim minimum of thirty (30) days oire SIX (6) MONTHS from on to become ABANDONE	ely filed will be considered time the mailing date of this co	ly. communication,			
Status								
1)🖂	Responsive to communication(s) filed on 20 D	ecember 2004		•				
2a)⊠		action is non-						
3)	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is							
	closed in accordance with the practice under E	x parte Quayl	e, 1935 C.D. 11, 45	3 O.G. 213.				
Dispositi	on of Claims	•						
4)🖂	Claim(s) <u>1-11,13-17,20-25 and 28</u> is/are pendi	ng in the appli	cation.					
1	. 4a) Of the above claim(s) is/are withdrawn from consideration.							
l —	Claim(s) <u>1-11,13-17 and 20-25</u> is/are allowed.							
6)⊠	6)⊠ Claim(s) <u>28</u> is/are rejected.							
7)	Claim(s) is/are objected to.		·					
8)□	Claim(s) are subject to restriction and/or	r election requ	irement.					
Applicati	on Papers							
9) ☐ The specification is objected to by the Examiner.  10) ☑ The drawing(s) filed on <u>02 August 2004</u> is/are: a) ☑ accepted or b) ☐ objected to by the Examiner.  Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).								
11)	Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).  11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.							
	nder 35 U.S.C. § 119				10-102.			
·	•		05.11.0.0.0.4404.					
	Acknowledgment is made of a claim for foreign ☐ All b)☐ Some * c)☐ None of:	pnonty under	35 U.S.C. § 119(a)	-(d) or (t).				
ا مارد		s have been re	polived					
	The state of the priority decarrence have been received.							
<ul> <li>2. Certified copies of the priority documents have been received in Application No</li> <li>3. Copies of the certified copies of the priority documents have been received in this National Stage</li> </ul>								
application from the International Bureau (PCT Rule 17.2(a)).								
* See the attached detailed Office action for a list of the certified copies not received.								
Attachment	(s)							
1) Notice	e of References Cited (PTO-892)	4) [	Interview Summary (					
2) Notice of Draftsperson's Patent Drawing Review (PTO-948)  3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)  Paper No(s)/Mail Date  5) Notice of Informal Patent Application (PTO-152)								
Paper	nation Disclosure Statement(s) (PTO-1449 or PTO/SB/08) No(s)/Mail Date	5) [ 6) [	Other:	atent Application (PTC	J-152)			
L U.S. Patent and Tr PTOL-326 (Re		tion Summary		t of Paper No./Mail D	ate 02232005			

#### **DETAILED ACTION**

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### Summary of changes in this action

I. The rejection to claims 1-11,13-17 and 20-25 have been overcome responsive to the amendment.

### Claim Rejections - 35 USC § 112

Claim 28 is rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

In claim 28, there is no support found for the newly recited language in lines 11-12. Lines 1-2 expressly recite that the method is for "pumping a charge in a semiconductor based charge pump". Since the specification expressly discloses that the "charge pump" is element 202 of Fig. 2, all of the steps recited in the method should pertain thereto. Since the language recited in lines 11-12 pertains to the operation of loop filter 204, which is separate from the "charge pump", it is misdescriptive to recite this step as being part of the method of "pumping a charge in a semiconductor based charge pump".

## Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claim 28 is rejected under 35 U.S.C. 103(a) as being unpatentable over Kawasaki (USPN 5,955,904) in view of Kobayashi et al. (USPN 5,936,455, newly cited). Kawasaki discloses, in

Figs. 3, 4 and 5 a circuit that will provide a method including the steps of "receiving first (CLK) and second (CKE) input signals at first (50) and second (63) switching transistors"; and "providing a substantially constant reference voltage (Vref) to first (51) and second (64) complementary transistors". The reference to Kawasaki does not expressly disclose that Vref is noise filtered. However, it is notoriously well known, such as taught in Figs. 5-6 of Kobayashi et al. to capacitor filter a reference voltage provide to a comparator. Such a configuration is known provides smoothing of the reference voltage. Therefore, it would have been obvious for one skilled in the art to provide capacitor filtering to Vref in the circuit to Kawasaki, such as taught by Kobayashi et al. for the expected advantage of smoothing.

Examiner has fully considered Applicant's remarks for the above rejection and has not found them to be persuasive. Firstly, the circuits of Figs. 4 and 5 of Kawasaki are expressly disclosed as being part of Fig. 3. Thus, they are not separate circuits. Additionally, Examiner is reading the entirety of Fig. 3 as being a "charge pump". As seen, with the above modification including filter of Vref, it would have been clear to one skilled in the art he Vref is further capable of being used as a "second output".

Claim 28 is rejected under 35 U.S.C. 103(a) as being unpatentable over Lee (USPN 5,889,437) in view of Abdi et al. (USPN 5,722,052). The reference to Lee discloses a circuit that will provide a method including the steps of "receiving first (UP) and second (DN) input signals at first (M41) and second (M43) switching transistors"; "providing a substantially constant reference voltage ( $\overline{UP}$  and  $\overline{DN}$  as modified below) to first (M42) and second (M44) complementary transistors" and "filtering noise (see lines 3-10 of Col. 5 of Abdi et al., as modified below)". Clearly, Vref is capable of being provided as a "second output". The reference to Lee discloses using complementary signals (UP,  $\overline{UP}$ , DN,  $\overline{DN}$ ) rather than using a

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single-ended signal and constant reference voltage. However, it is notoriously well known, as is disclosed by Abdi et al., complementary signals and a single-ended signal with a constant reference voltage (e.g., VREF) for use with a comparator are art-recognized equivalents and can be used in a charge pump circuit. The configuration of a single-ended signal and constant reference voltage is well known as having the advantage of being usable with a phase detector that generates a single-ended signal. Therefore, it would have been obvious for one skilled in the art to use a single-ended signal and constant reference voltage in place of the complementary signal for the expected advantage of being usable with a single-ended phase detector.

Examiner has fully considered Applicant's remarks for the above rejection and has not found them to be persuasive. Lines 3-10 of Col. 5 of Abdi et al. expressly recite that "V<sub>REF</sub> is preferably a DC voltage level compensated against variations in temperature and supply voltage". Examiner contends that one skilled in the art would deem this operation as reasonably encompass "filtering noise".

Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event,

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however, will the statutory period for reply expire later than SIX MONTHS from the date of this

final action.

Conclusion

Any inquiry concerning this communication or earlier communications from the

examiner should be directed to Terry Cunningham whose telephone number is 571-272-1742.

The examiner can normally be reached on Monday-Thursday from 7:30 to 6:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's

supervisor, Timothy P. Callahan can be reached on 571-272-1740. The fax phone number for

the organization where this application or proceeding is assigned is (703) 872-9306.

Information regarding the status of an application may be obtained from the Patent

Application Information Retrieval (PAIR) system. Status information for published applications

may be obtained from either Private PAIR or Public PAIR. Status information for unpublished

applications is available through Private PAIR only. For more information about the PAIR

system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR

system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

TC

February 23, 2005

Terry D. Cunningh Primary Examiner

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